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| APPLICATION NO.  | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|--|-------------|----------------------|---------------------|------------------|
| 10/614,928   | 07/08/2003  | Jin-Yuan Lee         | MEG00-009B          | 9220             |
| 7590   | 03/13/2006  |                      | EXAMINER            |                  |
| George O. Saile<br>28 Davis Avenue<br>Poughkeepsie, NY 12603 |             |                      | MENZ, DOUGLAS M     |                  |
|  |             |                      | ART UNIT            | PAPER NUMBER     |
|  |             |                      | 2891                |                  |

DATE MAILED: 03/13/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

|                              |                             |                  |  |
|------------------------------|-----------------------------|------------------|--|
| <b>Office Action Summary</b> | Application No.             | Applicant(s)     |  |
|                              | 10/614,928                  | LEE, JIN-YUAN    |  |
|                              | Examiner<br>Douglas M. Menz | Art Unit<br>2891 |  |

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

1) Responsive to communication(s) filed on 02 February 2006.

2a) This action is FINAL.                    2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

4) Claim(s) 25-44 is/are pending in the application.

4a) Of the above claim(s) 36-44 is/are withdrawn from consideration.

5) Claim(s) \_\_\_\_\_ is/are allowed.

6) Claim(s) 25-35 is/are rejected.

7) Claim(s) \_\_\_\_\_ is/are objected to.

8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on 08 July 2003 is/are: a) accepted or b) objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All    b) Some \* c) None of:

- Certified copies of the priority documents have been received.
- Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
- Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

1) Notice of References Cited (PTO-892)

2) Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_.

4) Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.

5) Notice of Informal Patent Application (PTO-152)

6) Other: \_\_\_\_\_.

## DETAILED ACTION

### *Election/Restrictions*

Applicant's election with traverse of Species I, claims 25-35, in the reply filed on 2/2/06 is acknowledged. The traversal is on the ground(s) that Applicant would face increased costs if the two species are separately examined. This is not found persuasive because it has been shown in Paper dated 12/29/05 that a restriction requirement was indeed proper.

The requirement is still deemed proper and is therefore made FINAL.

### *Claim Rejections - 35 USC § 102*

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 25-35 are rejected under 35 U.S.C. 102(e) as being anticipated by Taniguchi et al. (US 6489676).

Regarding claim 25, Taniguchi discloses an electronic package comprising:  
a first circuitry component having a top surface (4, Fig. 18);

a second circuitry component (4a, Fig. 18) over said top surface;  
an insulation layer (8, Fig. 18) covering said second circuitry component and said top surface; and a metal layer (42, Fig. 18) on said insulation layer.

Regarding claim 26, Taniguchi further discloses wherein said first circuitry component comprises a semiconductor chip (4, Fig. 18 and Col. 9).

Regarding claim 27, Taniguchi further discloses wherein said second circuitry component comprises a semiconductor chip (4a, Fig. 18 and Col. 9).

Regarding claim 28, Taniguchi further discloses a bump between said first and second circuitry components (6e, Fig. 18 and Col. 9).

Regarding claim 29, Taniguchi further discloses wherein said insulation layer (8, Fig. 18) comprises a portion between said first and second circuitry components and enclosing said bump (Fig. 18).

Regarding claim 30, Taniguchi further discloses wherein said insulation layer comprises a photosensitive material (Col. 9).

Regarding claim 31, Taniguchi further discloses a via (18, Fig. 18) through said insulation layer and connecting said first circuitry component and said metal layer.

Regarding claim 32, Taniguchi further discloses wherein said insulation layer has a top surface comprising a first region and a second region, said first region being over said second circuitry component, said second region being not over said second circuitry component, wherein said first and second regions are coplanar (Fig. 18).

Regarding claim 33, Taniguchi further discloses wherein said metal layer is on said first and second regions (Fig. 18).

Regarding claim 34, Taniguchi further discloses a bump over said metal layer (Fig. 12).

Regarding claim 35, Taniguchi further discloses wherein said second circuitry component comprising a top surface and a bottom surface facing said top surface of said first circuitry component, said insulation layer over said top surface of said second circuitry component (Fig. 18).

### ***Conclusion***

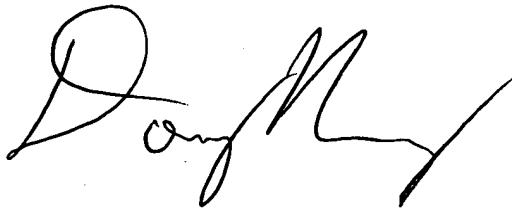
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Douglas M. Menz whose telephone number is 571-272-1877. The examiner can normally be reached on M-F 8-5.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bill Baumeister can be reached on 571-272-1722. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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